RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

New Scheme Based On AICTE Flexible Curricula

Computer Science & Information Technology, III-Semester

CSIT304 Digital Circuits & Systems

Unit 1: Review of number systems and number base conversions. Binary codes, Boolean algebra, Boolean functions, Logic gates. Simplification of Boolean functions, Karnaugh map methods, SOP-POS simplification, NAND-NOR implementation.

Unit 2: Combinational Logic: Half adder, Half subtractor, Full adder, Full subtractor, look-ahead carry generator,BCD adder, Series and parallel addition, Multiplexer – demultiplexer, encoder- decoder, arithmetic circuits, ALU

Unit 3 : Sequential logic: flip flops, D,T, S-R, J-K Master- Slave, racing condition, Edge & Level triggered circuits, Shift registers, Asynchronous and synchronous counters, their types and state diagrams. Semiconductor memories, Introduction to digital ICs 2716, 2732 etc. & their address decoding. Modern trends in semiconductor memories such as DRAM, FLASH RAM etc. Designing with ROM and PLA.

Unit 4 : Introduction to A/D & D/A convertors & their types, sample and hold circuits, Voltage to Frequency & Frequency to Voltage conversion. Multivibrators :Bistable, Monostable, Astable, Schmitt trigger, IC 555 & Its applications. TTL, PMOS, CMOS and NMOS logic. Interfacing between TTL to MOS.

Unit 5 : Introduction to Digital Communication: Nyquist sampling theorem, time division multiplexing, PCM, quantization error, introduction to BPSK & BFSK modulation schemes. Shannon's theorem for channel capacity.

References:

1. Morris Mano, Digital Circuits & Logic Design, PHI
2. Gothman, Digital Electronics, PHI
3. Tocci, Digital Electronics, PHI
4. Mavino& Leach, Digital Principles & Applications, PHI
5. R P Jain, "Modern Digital Electronics", Tata McGraw-Hill publishing company Ltd.
6. Simon Haykin, Introductionto Analog& Digital Communication, Wiley.
7. Lathi B.P., Modern analog& digital communication , Oxford University.
8. Taub and schilling, Digital Integrated electronics.

List of Experiments:

1. To study and verify the truth tables of various Logic gates
2. To verify the properties of NAND and NOR gates as Universal Building Blocks.
3. Simplification and implementation of a Boolean function
4. Implementation of basic Boolean arithmetic logic circuits such as Half-adder, Half-subtractor, Full adder and Full subtractor
5. Conversion from Binary to Gray and Gray to Binary code
6. To construct a binary multiplier using combinational logic and to verify with the truth table
7. To verify 2-bit Magnitude comparator for all possible conditions
8. Generation of various logical functions using 8-to-1 multiplexer
9. Construction of a 4-bit ripple counter and study of its operation
10. Operation of IC-555 Timer as Monostable, Astable and Bistable multivibrators
11. To characterize binary ladder type digital to analog (D/A) and analog to digital (A/D) convertor
12. Comparison of various Logic families.
13. Design and implementation of various types of flip-flops using JK flip-flop
14. To study natural sampling of continuous time waveforms using different sampling rates
15. To study Pulse-Code modulation with Time-division multiplexing (PCM-TDM)
16. To study generation and detection of BPSK and QPSK waveforms