RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

New Scheme Based On AICTE Flexible Curricula

Computer Science & Information Technology, V-Semester

Departmental Elective CSIT- 503 (B) Microprocessor and Interfacing

Course Objectives:

- To introduce basic concepts of microprocessor
- To introduce serial and parallel bus standards.
- To introduce programming in assembly language.
- To introduce basic concepts of interfacing memory and peripheral devices to a microprocessor.

UNIT –I:

Evolution of microprocessor, single chip micro computers, Micro processor Application, Microprocessor and its architecture, addressing modes, instruction, Instruction sets, Arithmetic and Logic Instruction, Program control instruction, Introduction –8086 family, procedure and macros, connection, Timing and Troubleshooting interrupt, 80286, 80836 and 80486 micro processor system concept.

UNIT –II:

Microprocessor Cycle, AIU, Timing and control Unit, Register data, Address bus, Pin Configuration, Intel 8086 instruction, Opcode and operands, limitation word size. Programming the microprocessor Assembly language, The Pentium and Pentium Pro Micro Processorwith features, Pentium II, Pentium III and Pentium –IV Microprocessor with software changes. Instruction set for Intel 8086, Introduction Intimation and data formats, Addressing modes, Status flags, Symbols and abbreviations, programming of microprocessors, Assembly language, high level language, areas of application of various languages, Stacks, Sub routines system, software, commands in assembly language, software Development, Debugging program, Modular programming, Structured programming, Top-down, Bottom-up design , MACRO microprogramming.

UNIT-III:

Assembly language programming with Examples like Addition of 8/16-bit Binary number, subtraction of 8/16 bit binary number, Address partitioning, addressing mode, type of addressing mode, memory and I/o interfacing, Data transfer schemes, Interfacing device and I/o devices I/o ports, Basic I/o Interfacing MDS, Micro controllers, I/o processor and co-processors ,Microcomputer Development system, Single chip micro computers, intel 8748 intel 8051, inter 8096, intel 8049intel 2920/2921, I/o processor UPI-425,UPI-41,42, Co-processor, math processor math co-processor –8087, 80287, 80387DX 803875x

UNIT –IV:

Bus Interface I/o port Addressing, decoding 8279, Programmable key board/display interface, 8254 Internal Timer, 16550 programmable communication interface A/D, 8259A Programmable Interrupt Controller, 8237 DMA Controller, Shared bus operation, disk Memory system Video display. ISA Bus, Extended ISA (EISA) and VESA Local Buses, Peripheral Component Inter Connect (Pc I) Bus, Parallel Printer interface (LPT) Universal serial Bus (USB) Accelerated graphics port (AGP),Programmable Communication interfere 8251 VSART CRT Controller 8275, 6854, Floppy disk Controller 8272, I/o processor 8089.

UNIT –V:

Memory Unit, RAM,SRAM, DRAM,ROM, PROM EPROM, EEPROM Nonvolatile RAM semiconductor Technology for memory, Shift register, Magnetic Memory, Tap, disc, main memory and secondary memory cache memory, program memory and Data Memory, Real and virtual memory Buses, memory Addressing capacity of CPU, processing speed of computer

Reference Books:

1. Douglas V Hall, "Microprocessors and interfacing –Programming & Hardware" TMH

2. Barry B. Brey, "The intel Microprocessor -8086", Pearson Education

3. Kenneth J.Ayala,"The 8086 Microprocessor: Programming & Interfacing The PC",CengageLearning

4. Krishna Kant,"Microprocessors and Microcontrollers", PHI Learning

- 5. A.K.Ray KM Bhurchandi, "Advanced Microprocessor and peripherals" McGraw Hill
- 6. R.S. Gaonkar,"Microprocessors and interfacing", TMH

Course Outcomes:

At the completion of the course, students will be able to...

- Explain the microprocessor's and Microcontroller's internal architecture
- Apply knowledge and demonstrate programming proficiency using the various addressing modes and data transfer instructions of the target microprocessor and microcontroller.
- Compare accepted standards and guidelines to select appropriate Microprocessor(8085&8086) and Microcontroller to meet specified performance requirements.
- Analyze assembly language programs
- Design electrical circuitry to the MicroprocessorI/Oports in order to interface the processor to external devices.
- Evaluate assembly language programs