

**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL**

**New Scheme Based On AICTE Flexible Curricula**

**Electrical & Electronics Engineering, VII-Semester**

**EX-706 Major Project-I**

**GUIDELINES**

The objectives of the course 'Major Project-I' are To provide students with a comprehensive experience for applying the knowledge gained so far by studying various courses. To develop an inquiring aptitude and build confidence among students by working on solutions of small industrial problems. To give students an opportunity to do something creative and to assimilate real life work situation in institution.

To adapt students for latest developments and to handle independently new situations.

To develop good expressions power and presentation abilities in students.

The focus of the Major Project is on preparing a working system or some design or understanding of a complex system using system analysis tools and submit it the same in the form of a write-up i.e. detail project report. The student should select some real life problems for their project and maintain proper documentation of different stages of project such as need analysis, market analysis, concept evaluation, requirement specification, objectives, work plan, analysis, design, implementation and test plan. Each student is required to prepare a project report and present the same at the final examination with a demonstration of the working system (if any).

The faculty and student should work according to following schedule:

- i) Each student undertakes substantial and individual project in an approved area of the subject and supervised by a member of staff.
- ii) The student must submit outline and action plan for the project execution (time schedule) and the same be approved by the concerned faculty.
- iii) At all the steps of the project, students must submit a written report of the same.